

## ZAHID UR RAHMAN

s/o Fazal Ur Rahman Khan

### PERSONAL DATA

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### CAREER OBJECTIVES

Definitely curious to join a dynamic institution/organization for greatest usage of my capabilities, aptitudes and my devotion.

### EXPERIENCE

#### ✓ Lab Engineer at University of the Poonch Rawalakot (UPR)

From May 10, 2017 to date.

**Subjects:** FPGA based system design, intro to programming C++, Microprocessors, Digital signal processing.

#### ✓ Lecturer at PSIT in sultana foundation From Aug 21, 2016 to May 10, 2017.

**Subjects:** intro to computer, Microprocessors, Microcontroller, communication system.

#### ✓ Research associate at Australasia technologies 1 Oct 2015 to date.

##### Description:

- ✓ Design and develop new products in accordance to customer specifications.
- ✓ Modify existing product designs.
- ✓ Provide technical support to manufacturing.
- ✓ Supervise verification and validation of new product designs.
- ✓ planning and organizing;
- ✓ problem-solving activities;
- ✓ work in a logical, systematic manner;
- ✓ Measuring, grading and feeding batches of raw materials into production machinery.
- ✓ operating production line equipment, (CNC, PLC, Micro controllers etc )
- ✓ apply required maintenance and programming of production line equipment, (CNC, PLC, Micro controllers etc )
- ✓ estimating costs and setting the quality standards;

#### ✓ Lab Engineer at Bahauddin Zakariya University Lahore campus (BZU Lahore) from 3rd march 2015 to 29 Sep 2015.

**Duties:** Lab demonstration, research activities, teaching

**Subjects:** Measuring instrument, Microcontroller and Microprocessor.

**Lab Demonstration:** C++, Measuring instrument, AUTOCAD, Microcontroller.

✓ **Visiting junior lecturer at Mirpur University of Science and Technology**

(MUST) Mirpur AJK from October 29, 2013 to January 3, 2015.

**Subjects:** Measuring instrument, intro to programming C++, Electronic Devices, Microcontroller and Embedded Processors, Digital Electronics.

## RESEARCH WORK

- ✓ Design of printer for the fabrication of passive electronic components on unconventional surfaces for electronic applications funded by ORIC department, cost 0.248 million PKR.
- ✓ Design of dip coater machine for the Nano particulate thin films fabricated by layer by layer deposition for sensor applications funded by ICT R&D, cost 60,000 PKR.
- ✓ Solar power system for electric campus MUST University approved by HEC, cost 1 million PKR.
- ✓ Efficient campus lighting system using LEDs approved by ORIC department, cost 0.250 million PKR.
- ✓ Reduce the fuel cast of trains system using rotational and friction forces funded by ICT R&D, cost 86,000 PKR.
- ✓ Design of low-cost touch panel for house hold appliances
- ✓ Currently working on research paper of my M.Phil thesis.

## ACADEMIC QUALIFICATION

Certificate/ Degree Name	Degree Title & major subjects	Year of Passing	%age	Board/University	Institute Name
S.S.C	Science	2001	48	BISE, Mirpur AJ&K.	Govt Pilot High School Bagh (AJ&K)
One year diploma	Vocational training course	2003/04	77.5	National Training Board	Govt vocational training institute Bagh AJ&K
Diploma of Associate Engineering	Electronics	2009	84	Punjab board of technical education	Rawalpindi Swedish institute of technology
B.Sc.	Electrical Engineering	2013	78	MUST, Mirpur	Mirpur University of Science & Technology AJ&K.
M.Sc	Electronics	Complete		MUST, Mirpur	Mirpur University of Science & Technology AJ&K.

## INDUSTRIAL TRAINING

## **Description:**

- ✓ **Internship at Mangla Power Station**  
Study tour of spill way, grid station learning Power generation, transmission and distribution, involving reports handling, (8 weeks).
- ✓ **Internship at PTCL in DSL Broad Band section**  
Involving Main distributing Frame (MDF) patching, Optical Network unit (ONU) Patching, Line qualification test, premises installation and trouble shooting.

## **FINAL YEAR PROJECT(FYP)**

Project title:

HYBRID TRAIN

Description:

- ✓ An Electric train which runs on renewable energy sources as well as fuel.
- ✓ An Efficient fuel and electric energy saving system.
- ✓ Wind system solar system and back generator are used to produce electricity.

## **SEMESTER WISE PROJECTS**

- 1<sup>st</sup> semester: regulated power supply.
- 2<sup>nd</sup> semester: automatic door using card system.
- 3<sup>rd</sup> semester: Amplifiers & Oscillators.
- 4<sup>th</sup> semester: Small DC motor.
- 5<sup>th</sup> semester: Multipurpose Regulated power supply.
- 6<sup>th</sup> semester: UPS.
- 7<sup>th</sup> semester: Security System Using Holography.
- 8<sup>th</sup> semester: Ultrasonic Robot.

## **SOFTWARE TOOLS**

- ✓ Python (Spyder)
- ✓ C / C++ / Embedded C / MikroC / arduino / pic / AVR.
- ✓ Matlab.
- ✓ Labview.
- ✓ PCB Design / eagle CAD.
- ✓ Proteus.
- ✓ Electronic work bench.
- ✓ LIVE wire.
- ✓ Circuit maker.
- ✓ AutoCAD.
- ✓ 3D Studio Max.
- ✓ Autodesk Maya

## **COMPUTER SKILLS**

- ✓ An excellent command over MS Office including MS Word, MS Excel and MS PowerPoint.
- ✓ Good hands on experience of latest windows operating system.
- ✓ An excellent command over using the internet.

## **SHORT COURSES & WORKSHOPS**

Short courses

- ✓ Microcontroller (1 month)
- ✓ Carpenter course (1 month)

#### Workshops

- ✓ Microcontroller (organizer + presenter) (10 days)
- ✓ Programmable Logic controller(PLC) (30 days)
- ✓ Auto CAD(organizer) (10 days)
- ✓ MATLAB(organizer) (15 days)
- ✓ PIC controller(organizer + presenter) (15 days)
- ✓ The world of Robots(presenter) (15 days)
- ✓ Time Management(organizer) (03 days)
- ✓ Illusion of words (01 days)

### INTEREST

#### Description:

- ✓ Work on Electronic Projects.
- ✓ Study books.

### LANGUAGES

- ✓ Urdu, English, Punjabi

### REFERENCE

It will be furnished on Request.