

Syed Qasim Gilani

CONTACT

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Muzaffarabad, Aj&K



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SKILLS

Matlab	Labview
Simulink	Multisim
LT SPICE	DSCH
FPGA	Xilinx
Proteus	HFSS
C++	MS Office

LANGUAGES

English

SUMMARY

I am looking for lecturer position in Oman.

WORK EXPERIENCE

01/2016 - present

Lab Engineer, UAJ&K, Muzaffarabad, AJK, Pakistan

Responsibilities:

- Establishment of Undergraduates labs at Department of Electrical Engineering, UAJ&K
- Preparation of lab manuals for undergraduate courses.
- Lab instructor for undergraduate courses.

Research Experience:

Papers:

1. **Gilani, S.Q., Hassan, S.A., Pervaiz, H. and Ahmed, S.H.,** 2019, February. Performance Analysis of Flexible Duplexing-enabled Heterogeneous Networks Exploiting Multi Slope Path Loss Models. In *2019 ICNC IEEE*.

EDUCATION

2009 – 2013

BS Electronics Engineering, Comsats IIT, Abbottabad.

CGPA 3.4

FYP: Simulation and Implementation of Digital Watermarking of Audio and Text on FPGA.

2014 - 2018

MS Electrical Engineering, NUST, Islamabad.

CGPA 3.5

MS Thesis: Performance Analysis of Flexible Duplexing Heterogeneous Networks using Multi Slope Path Loss Models.

REFERENCES

References available on request